

FIG. 1

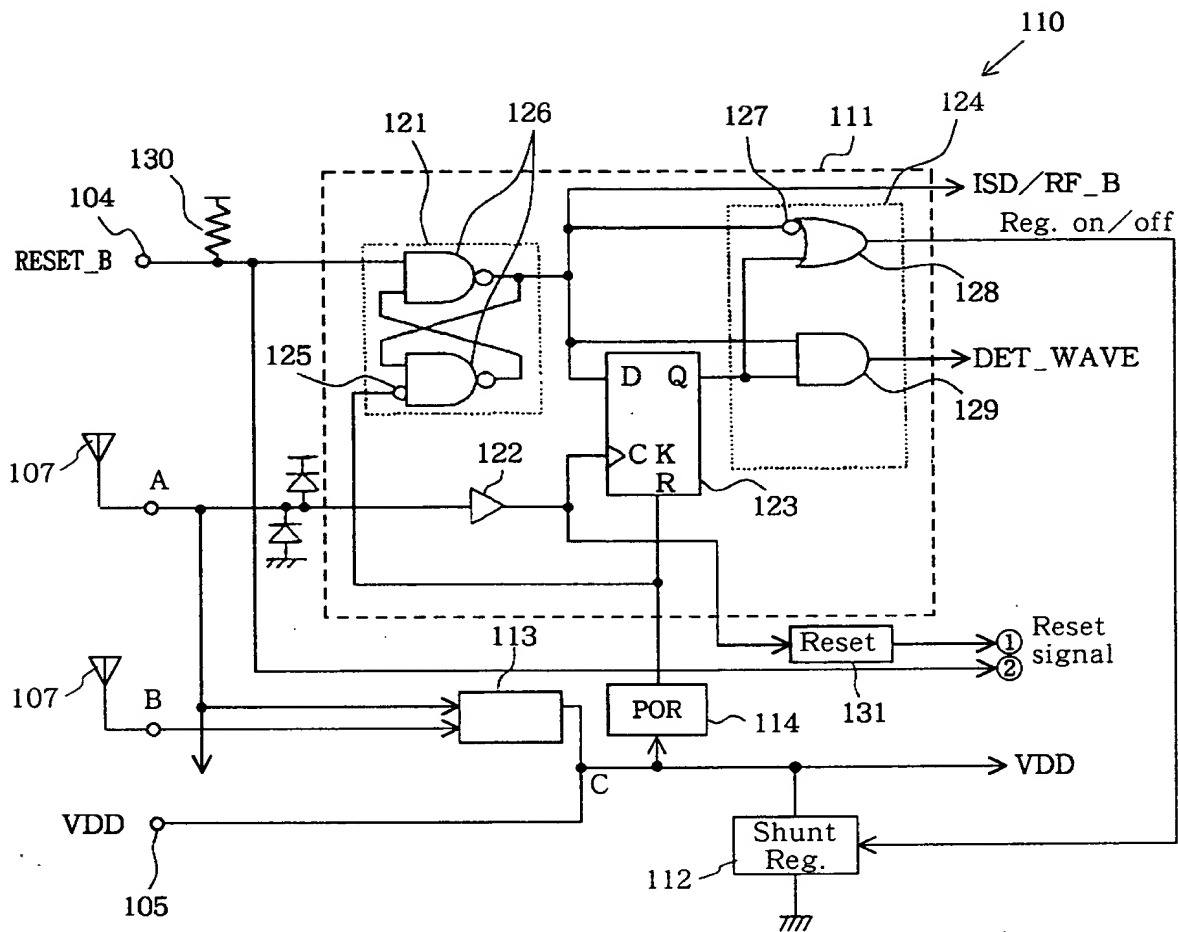


FIG. 2

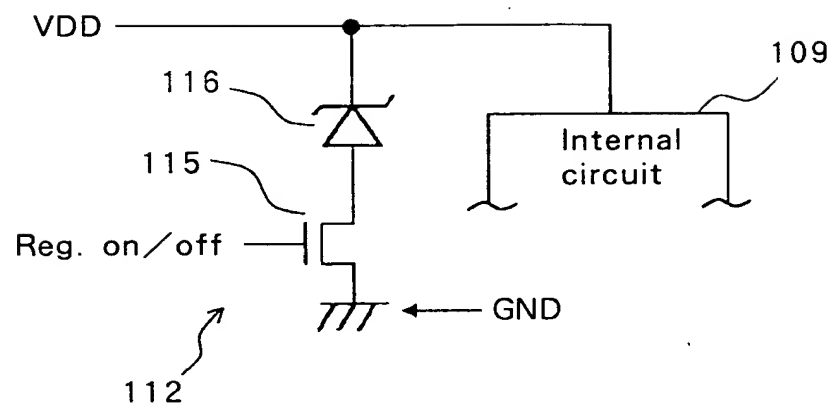


FIG. 3

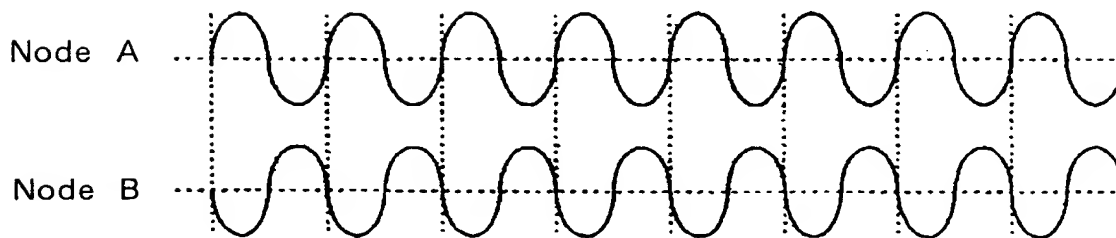


FIG. 4

Node A

Node B

Internal
VDD

POR

RESET_B

ISO/RF_B

Reg. on/off

F/F②

DET_WAVE

Undefined

FIG. 5

VDD

RESET_B

POR

ISO/RF_B

Reg. on/off

F/F②

DET_WAVE

1

0

0

0

FIG. 6

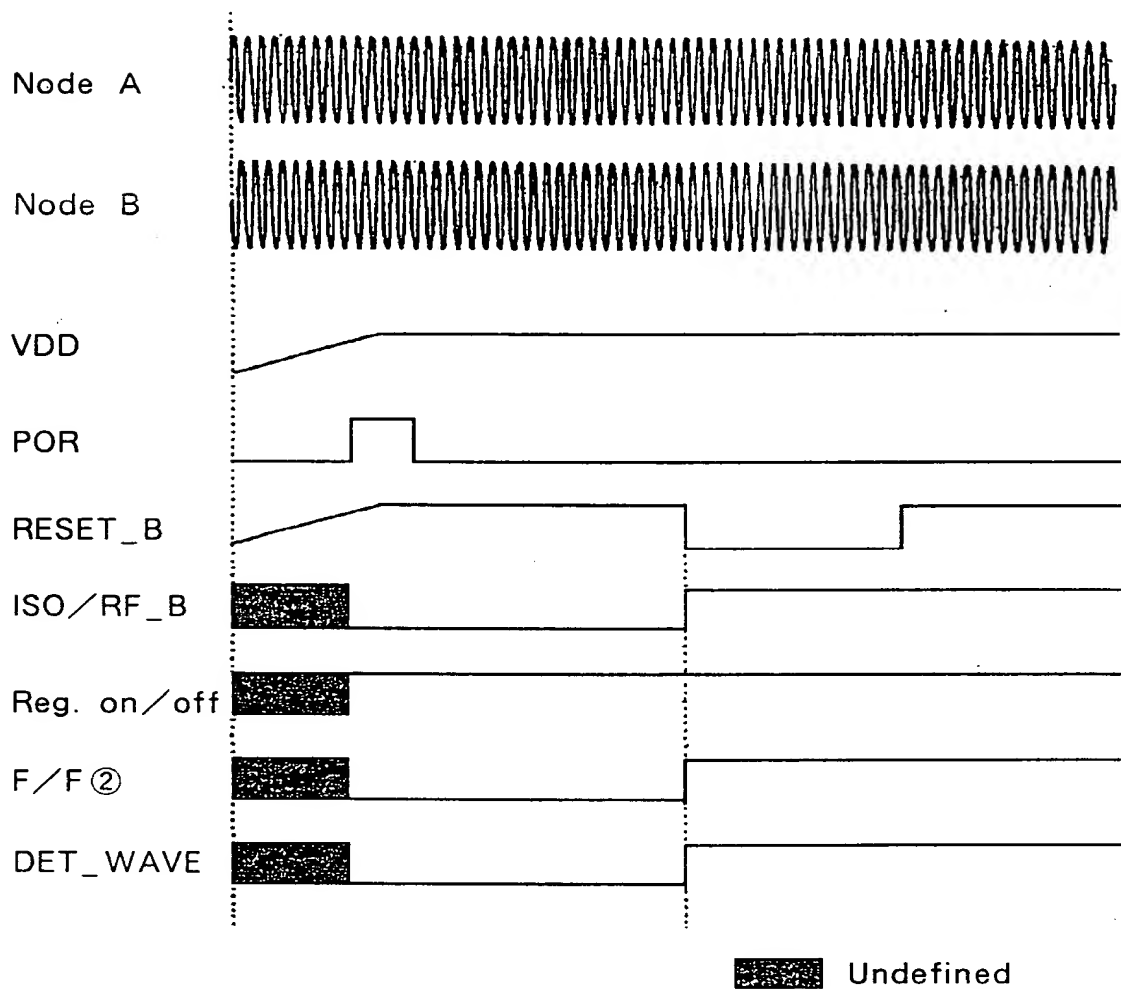


FIG. 7

VDD

RESET_B

POR

ISO/RF_B

Reg. on/off

F/F②

DET_WAVE

Node A

Node B

FIG. 8